

ABSTRACT

5 In a liquid crystal display comprising a liquid
crystal panel with an antiferroelectric liquid crystal
sandwiched between a pair of substrates having scanning
electrodes and signal electrodes, a display driving
voltage waveform in which the peak value of a scanning
voltage waveform applied during a selection period is set
equal to the peak value of a scanning voltage waveform
10 applied during a non-selection period is output as a
layer structure controlling voltage waveform for a
predetermined length of time. The liquid crystal panel
includes a temperature sensor, and in accordance with the
information output from the temperature sensor, the
15 display driving voltage waveform with both peak values
set equal is output as the layer structure controlling
voltage waveform for the predetermined length of time.